

# APPLICATION NOTE

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(Taking charge of SiRF by  
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**SUBJECT: Electro Static Sensitivity for RA30H4552M1 and RA30H4047M1**

**GENERAL:**

RA30H4552M1 and RA30H4047M1 use MOS FET device.

MOS FET devices have lower surge endurance compared with silicon bipolar devices.

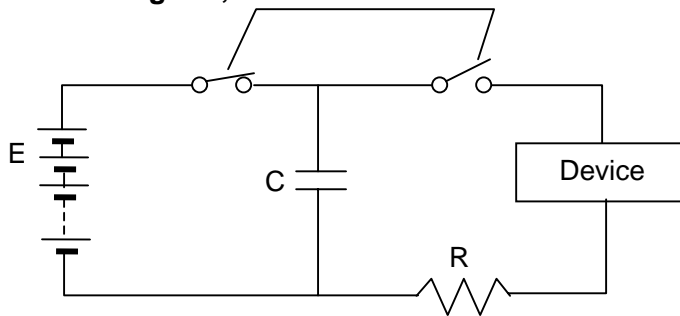
And there is a possibility of burn-out when static electricity or surge is added to devices.

This application note shows the test results of the electro static discharge level for

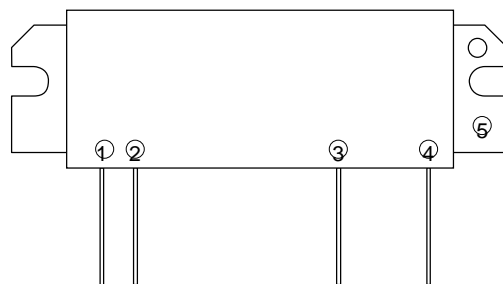
RA30H4552M1 and RA30H4047M1.

**1. ELECTRO STATIC DISCHARGE TEST RESULTS:**

**-1. Test Block Diagram;**

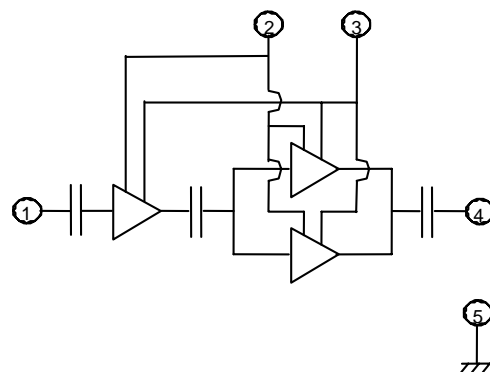


**-2. Pinning;**



- 1 RF Input ( $P_{in}$ )
- 2 Gate Voltage ( $V_{GG}$ )
- 3 Drain Voltage ( $V_{DD}$ )
- 4 RF Output ( $P_{out}$ )
- 5 RF Ground (Flange)

**(Block Diagram)**



**-3. Human Model Test Results;**

**[Type number: RA30H4047M1 (Po>30W @400-470MHz, Vdd=12.5V)]**

Test Conditions are;

C=100pF, R=1.5KΩ, 3 times discharge for one Voltage,

E=100V step increasing (Max. 6000V)

Terminal	Polarity	Sample NO.	Destroyed Voltage(V)	Polarity	Sample NO.	Destroyed Voltage(V)
Pin to Flange	+	1	1400	-	1	-2700
		2	1400		2	-2600
Vgg to Flange	+	1	2600	-	1	Over -6000
		2	2600		2	Over -6000
Vdd to Flange	+	1	Over 6000	-	1	Over -6000
		2	Over 6000		2	Over -6000
Pout to Flange	+	1	Over 6000	-	1	Over -6000
		2	Over 6000		2	Over -6000

NOTE: Test of RA30H4552M1 that is derivative from RA30H4047M1 is omitted for the same type under this test.